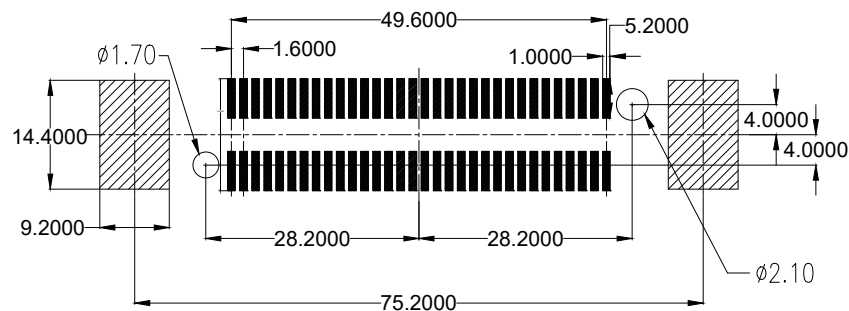
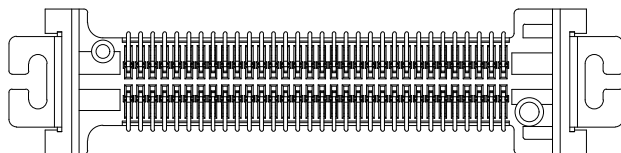
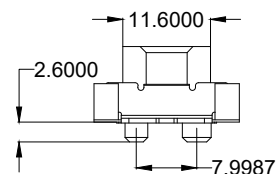
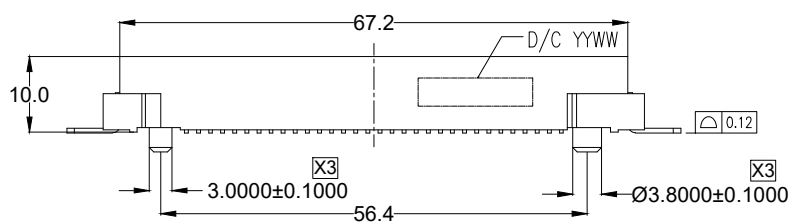
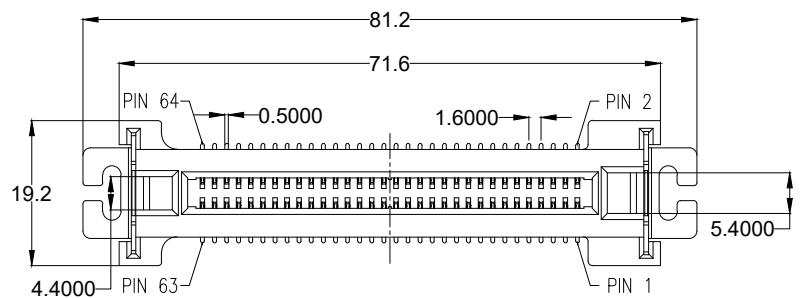


PROPRIETARY INFORMATION
COMPANY CONFIDENTIAL

REV.	ECN. NO.	DESCRIPTION	ENG	DATE
X1		INITIAL	AXL	2/17/2016
X2		EDITED NOTES 3	AXL	4/30/2018
X3		CHANGE THE TOLERANCE	TERRY	6/8/2020
X4		CHANGED 5G TO 8G	ZAKK	6/19/2023



RECOMMENDED PCB LAYOUT
TOLERANCE:±0.05mm

NOTES :

1. MATERIAL
HOUSING: LCP, UL94V-0, BLACK
TERMINAL: PHOSPHOR BRONZE
BOARDLOCK: BRASS
2. PLATING:
TERMINAL:
CONTACT AREA: GOLD FLASH PLATING
SOLDER TAIL: 80u" TIN PLATING
UNDER PLATING: 30u" NICKEL OVERALL
BOARDLOCK: 30u" NICKEL PLATING
3. CURRENT RATING:
 - a). FOR ALL PINS CONNECTED: 0.5A.
 - X2** b). FOR 6 CONSECUTIVE PINS CONNECTED: 1.0A.
4. DIELECTRIC WITHSTANDING VOLTAGE: 500VAC FOR 1 MINUTE.
5. INSULATION RESISTANCE: 100Mohms min.
- 4** 6. 8G GEN 3 PCIE min. COMPLIANT.
7. DESIGN TO MATE WITH 110-10007.
8. TRAY PACKAGING.
- 4** 9. STACKING HEIGHT IS 22mm OR 28mm

GENERAL TOLERANCE X ± 0.50 .X ± 0.40 .XX ± 0.30 .XXX ± 0.20		ANGLE TOLERANCE X° ± 5.0° .X° ± 3.0° .XX° ± 2.0° .XXX° ± 1.0°		DRAWN AXL		DATE 2/17/16'		 genesis connected solutions	
MATL				CHECKED		DATE			
FINISH				APPROVED		DATE		TITLE. HI-STACK B TO B 8Gbps FEMALE INTERFACE	
SCALE <i>N/A</i>		UNIT <i>mm</i>						PART NUMBER. 110-10008-01	
SHEET 1 OF 1						DRAWING NO. 110-10008		SIZE <i>A4</i> REV. <i>X4</i>	